



VHDL-2008: Just the New Stuff (Systems on Silicon)

By Peter J. Ashenden, Jim Lewis

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Editorial Review

About the Author

Peter J. Ashenden received his B.Sc.(Hons) and Ph.D. from the University of Adelaide, Australia. He was previously a senior lecturer in computer science and is now a Visiting Research Fellow at the University of Adelaide. His research interests are computer organization and electronic design automation. Dr. Ashenden is also an independent consultant specializing in electronic design automation (EDA). He is actively involved in IEEE working groups developing VHDL standards, is the author of *The Designer's Guide to VHDL* and *The Student's Guide to VHDL* and co-editor of the Morgan Kaufmann series, *Systems on Silicon*. He is a senior member of the IEEE and a member of the ACM.

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