



A First Course in Digital Systems Design: An Integrated Approach

By John P. Uyemura

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This book provides a new paradigm for teaching digital systems design. It puts forth the view that modern digital logic consists of several interacting areas that combine in a cohesive fashion. This includes traditional subjects such as Boolean algebra, logic formalisms, Karnaugh maps, and other classical topics. However, it goes beyond these subject areas by including VHDL, CMOS, VLSI and RISC architectures to show what the field looks like to a modern logic designer. Modern digital design is no longer practiced as a stand-alone art. The integrated approach used in this book is designed to ensure that graduating engineers are prepared to meet the challenges of the new century.

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Editorial Review

About the Author

John P. Uyemura is Professor of Electrical and Computer Engineering, late of Georgia Institute of Technology.

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