



Schaum's Outline of Computer Architecture (Schaum's Outlines)

By Nick Carter

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- A problem/solution manual, integrating general principles and laboratory exercises, that provides students with the hands-on experience needed to master the basics of modern computer system design
- Features more than 200 detailed problems, with step-by-step solutions; many detailed graphics and charts; chapter summaries with additional "rapid-review" questions; and expert sidebar tips
- Describes analytical methods for quantifying real-world design choices regarding instruction sets, pipelining, cache, memory, I/O, and other critical hardware and software elements involved in building computers
- An ideal educational resource for the more than 70,000 undergraduate and graduate students who, each year, enroll in computer architecture and related courses

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Editorial Review

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- Get the big picture without poring over lengthy textbooks

Schaum's Outlines give you the information your teachers expect you to know in a handy and succinct format without overwhelming you with unnecessary jargon. You get a complete overview of the subject. Plus, you get plenty of practice exercises to test your skill. Compatible with any classroom text, Schaum's let you study at your own pace and remind you of all the important facts you need to remember fast! And Schaum's are so complete, they're perfect for preparing for graduate or professional exams.

Inside, you will find:

- Overview of hardware and software design elements in computer systems
- Concise explanations of hardware and software interface
- Complete details on the efficient use of system resources
- Solved problems in: instruction sets, pipelining, cache, memory, I/O and more!

If you want top grades and a thorough understanding of computer architecture, this powerful study tool is the best tutor you can have!

Chapters include:

- Introduction
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- Computer Arithmetic
- Machine Organization
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- Pipelining
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- Introduction to Memory Systems

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- Introduction to Multiprocessors

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About the Author

Nick Carter, Ph.D. (Urbana, IL) is an assistant professor at the University of Illinois, Urbana-Champaign. He holds a doctorate in computer science from MIT, has worked at IBM's Yorktown, NY, research facility and was the memory system architect on MIT's prestigious M-Machine project.

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